

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	4517	sram and (capacitor\$1 same voltage\$1) and (switch\$3 or transistor\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 13:13
L2	2523	sram and (capacitor\$1 same voltage\$1) and ((switch\$3 or transistor\$1) same control\$4 same signal\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 13:09
L3	1471	sram and (capacitor\$1 same voltage\$1) and ((switch\$3 or transistor\$1) same (control\$4 near signal\$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 13:14
L4	209	sram and (capacitor\$1 same voltage\$1) and ((switch\$3 or transistor\$1) same (control\$4 near signal\$4) same read\$3 same writ\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 13:13
L5	6	sram and (capacitor\$1 same voltage\$1) and ((second\$2 near (switch\$3 or transistor\$1)) same (control\$4 near signal\$4) same read\$3 same writ\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 13:13
L6	1353	sram and (capacitor\$1 same voltage\$1) and (second\$2 near (switch\$3 or transistor\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 13:14
L7	171	sram and (capacitor\$1 same voltage\$1) and ((second\$2 near (switch\$3 or transistor\$1)) same (control\$4 near signal\$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 13:15
L8	1945	(365/154.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 13:15
L9	161	1 and 8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 13:15

L10	73	2 and 8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 13:15
L11	45	3 and 8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 13:15
L12	13	4 and 8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 13:15
L13	95	6 and 8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 13:15
L14	9	7 and 8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 13:15